



TAYYABA PARVEEN

Electrical Engineer

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Q-Type 5/11 near Zubaida Academy Junior Public School, Bhakkar

EDUCATION

Electrical Engineering Bachelors
Namal University, Mianwali
2020 - 2024
3.10 CGPA

FSC Pre-Engineering
Punjab College, Bhakkar
2018 - 2020
989/1100 Marks

Matric
Dar-e-Arqam School, Bhakkar
2016 - 2018
1074/1100 Marks

SKILLS AND SOFTWARES

Verilog, C/C++, Assembly, SystemVerilog

Linux, Logisim, OpenLane, Xilinx ISE, Yosys

Magic, Ngspice, Qflow, Open PDKs, Vivado

Iverilog, GTKwave, Verilator, Xschem

LANGUAGE

Urdu

English

ABOUT ME

Electrical Engineering Undergraduate Student in 4th year of my education at Namal University, Mianwali. With a deep interest in IC Design and Semiconductor industry looking for a platform where I can dedicate myself to further strengthen and enhance my skills.

PROJECTS

Design and Implementation of a Pipelined RISC-V Processor using RV32IC Support

Final Year Project

Design and Verification (Functional and Formal) of 32 bit Pipelined RISC-V Core PicoRV32 and FPGA porting

8-Bit CPU

Computer Architecture

Designed an 8-bit CPU having Register file, Memory, PC and Decoder etc. on Logisim

Single Cycle RISC-V Processor

Computer Architecture-Modelsim

Designed a 32-bit RISC-V single cycle processor using Modelsim and verified using Iverilog and GTKWave

Design and Implementation of ALU

Digital Logic Design

Designed an ALU using Modelsim and Logisim and also implemented on Breadboard

WORK EXPERIENCE

- Supercomputing and Parallel Programming, Spring School 2024 Namal University Mianwali
- Chip Design Summer School 2023 Namal University, Mianwali
- IC Design Internship CESD Namal University, Mianwali