



Maheen Gul

Electrical Engineering Graduate

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Highly motivated Electrical Engineering graduate with a passion for hardware design and application development. Proven ability to design and implement novel algorithms and digital circuits. Experienced in project leadership and team management.

EDUCATION

Study Program

Namal University Mianwali

10/2020 - 07/2024

3.31 CGPA

Courses

- Computer Architecture
- VLSI Design
- Digital Image Processing
- Digital Signal Processing

WORK EXPERIENCE

Verilog and Linux internship - CESD

10xEngineers

06/2022 - 08/2022

Remote

Achievements/Tasks

- Gained Proficiency in UNIX commands through hands on experience with Linux (Ubuntu) Operating System.
- Developed a strong understanding of Verilog (Hardware Description Language) and designed various IC with Verilog programming

Deep Learning Internship

RoboArt

08/2023 - 09/2023

Lahore

Achievements/Tasks

- Learnt the fundamentals of Python Programming
- Got deeper understanding of Artificial Intelligence and Machine Learning Algorithms
- Worked on a Machine Learning Model for University Admission Systems

Internship

WAPDA Power Plant - Chashma

05/2023 - 07/2023

Achievements/Tasks

- Acquired firsthand knowledge of power plant operation including the integrated functionality of various sections to meet electricity demands, handling power breakdown emergencies through on site training.

Backend Developer

Fiverr

01/2023 - Present

Achievements/Tasks

- Delivered Exceptional results as a Mern stack Developer, consistently receiving 5 star from clients across the globe and working in long term with them.

SKILLS

UNIX

RISCV Architecture

Iverilog

Verilator

C/C++

GCC Toolchain

Matlab

Autocad

Python Basics

Gem5

Magic

Nodejs

Javascript

PERSONAL PROJECTS

Trojan Detection in RISCV - FYP (10/2023 - 06/2024)

- Designed an algorithm that not only detect Trojans in RISCV based system but also prevent them from executing.

32-bit Pipelined RISCV Processor (01/2024 - 02/2024)

- Developed a 5-stage, 32-bit pipelined processor in verilog from scratch

Music and Speech Classification in Matlab

- Digital Signal Processing project

Student Record Management (C++)

- save, edit & delete records of new students, calculate GPA & CGPA, search and show records or certain records.

ORGANIZATIONS

Vice President - Namal Environmental Society

(10/2022 - 07/2023)

Oversaw the Society's operations by conducting various events, plantation drives and seminars.

Head of Documentation - IEEE Namal

(08/2021 - 04/2022)

Directed team to deliver accurate and well written documents.

CERTIFICATES

Spring School- Supercomputing and Parallel Programming

3-days workshop in which we learnt the benefits of parallelism in High Performance Computing

INTERESTS

Coding

Table Tennis

Badminton

Hiking