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Gujranwala, Pakistan



Highly motivated Electrical Engineering graduate with a passion for hardware design and application development. Proven ability to design and implement novel algorithms and digital circuits. Experienced in project leadership and team management.

EDUCATION

O Study Program Namal University Mianwali

10/2020 - 07/2024

3.37 CGPA

Courses

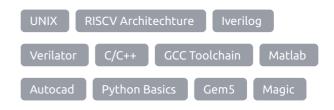
- Computer Architecture

VLSI Design

- Digital Image Processing

- Digital Signal Processing

SKILLS



WORK EXPERIENCE

Verilog and Linux internship - CESD 10xEngineers

06/2022 - 08/2022

Remote

- Achievements/Tasks
- Gained Proficiency in UNIX commands through hands on experience with Linux (Ubuntu) Operating System.
- Developed a strong understanding of Verilog (Hardware Description Languege) and designed various IC with Verilog programming

Deep Learning Internship RoboArt

08/2023 - 09/2023

Lahore

- Achievements/Tasks
- Learnt the fundamentals of Python Programming
- Got deeper understanding of Artificial Intelligence and Machine Learning Algorithms
- Worked on a Machine Learning Model for University Admission Systems

Vice President

SDS- Namal SKill and Development Society

10/2023 - 06/2024

- Achievements/Tasks
- Oversaw the Society's operations by conducting various workshops, hands-on trainings and seminars.
- Led and managed my team, by focusing on enhancing the technical and professional skills of students.

Operational Manager

IEEE - Namal

09/2022 - 02/2023

PERSONAL PROJECTS

Trojan Detection in RISCV - FYP (10/2023 - 06/2024)

 Designed an algorithm that not only detect Trojans in RISCV based system but also prevent them from executing.

32-bit Pipelined RISCV Processor (01/2024 - 02/2024)

 Developed a 5-stage, 32-bit pipelined processor in verilog from scratch

Music and Speech Classification in Matlab

Digital Signal Processing project

Student Record Management (C++)

 save, edit & delete records of new students, calculate GPA & CGPA, search and show records or certain records.

Alarm, Clock And Temperature Sensor

 The project asks user to set clock time and alarm time. Once it is set, it starts telling time and temperature simultaneously.

CERTIFICATES

Spring School- Supercomputing and Parallel Programming

3-days workshop in which we learnt the benefits of parallelism in High Performance Computing

INTERESTS

Coding	Table Tennis	Badminton	Hiking
Gaming			