SAMINA YASMIN

03027264171 saminayasmin2020@namal.edu.pk Village chah hussain wala, Tehsil and District Mianwali Mianwali, Pakistan

SUMMARY

An up-and-coming electrical engineering student with a keen eye for detail and a passion for innovation with a solid foundation in circuit analysis, power systems, control theory, and computer Architecture. A skilled programmer working with C, C++, Verilog, System Verilog, and MATLAB. Having the know-how to tackle complex engineering challenges. A team player with excellent communication skills. Keep indulged in the RISC-V and its related software in Free time.

EDUCATION

Namal University Mianwali

BS Electrical Engineering. CGPA: 3.42

2020 – 2024

Superior Group of Colleges Mianwali Campus

Intermediate; Sargodha Board Result: 92.00%

2018 – 2020

EXPERIENCE

Machine Learning | INTERNSHIP

Takbeez

August 2023 - September 2023 Lahore,Pakistan

Action Detection

Movie Recommendation System

Book Recommendation System

Sms Spam Classifier

SOFTWARES

Softwares:

- Cisco Packet Tracer
- Icarus Verilog
- GCC Tool Chain
- Qflow
- Magic Tool
- Irsim
- MATLAB/Simulink
- Ltspice
- ETAP
- Jupyter Notebook/Google Colab
- Microchip Studio
- Verilator
- Modelsim
- Vivado
- Ngspice
- Espresso
- Pspice
- AutoCAD
- Visual Studio

SKILLS

- C/C++
- Verilog HDL
- System Verilog
- Microcontroller
- Python
- Assembly
- Circuit Design
- Problem Solving
- Technical writing
- FPGA
- Digital and Analogue Electronics
- Programming

INTEREST

Computer Architecture | Digital Logic Design | VLSI | DIP

PROJECTS

Arithmetic and Logic Unit | DLD

- Built a 4-bit ALU working as a calculator using logic gates.

Automatic Roll No. Generator | DSD

- Designed the automatic roll no. generator and displayed using the seven-segment.

Data Management

- Applied data analysis on the COVID data.

Home Automation | EDC

- Designed 8 sensors using BJTs and some other electrical components that automated the home.

Home Automation | Embedded Systems

- Designed automated home using sensors and microcontroller atmega328p.

Electrical Transformer on AutoCAD | ED

- Designed Electrical Transformer on AutoCAD.

Software House Network | CCN

- Designed complete network on Cisco Packet Tracer.

Payroll management system | DSA

- Using concepts of OOP, data structure and C++ designed the payroll system of the company.

Single Cycle Processor | CA

- Designed and implemented the SCP using Verilog, meeting all MIPS ISA specifications.

Pipelined Processor | CA

- Designed the pipelined processor in Verilog capable of executing all the instructions.

Calculation of speed of the Cricket Ball by Video Data

- Using machine learning and deep learning concepts measured the speed of the ball using the video data.

Adding Bit manipulation extension in SWeRV core of RISC V Processor |FYP

- Making SweRV efficient for data security, IOT and other embedded applications by integration.

CERTIFICATION

- **Basics of Python Programming**: OpenWeaver
- **Open Source Technology**: UET Lahore
- **Machine Learning and Deep Learning**: Coursera