

Anousha Malik

Electrical Engineer

Innovative engineer with experience in IC design, including SoCs and FPGAs. Skilled in leadership and management, with a proven ability to effectively lead and organize teams.

✉ anoushamalik89@gmail.com

📍 Mianwali, Pakistan

🌐 github.com/anoushamalik

☎ 03154602757

🌐 linkedin.com/in/anousha-malik-a6924b240

🎧 medium.com/@anoushamalik

EDUCATION

Pre-Engineering

Superior Group of Colleges

09/2018 - 02/2020

BS Electrical Engineering

Namal University Mianwali

11/2021 - Present

WORK EXPERIENCE

Jr. Research Assistant

Center for AI & Big Data (CAID)

09/2024 - Present

Achievements/Tasks

- Gained in-depth knowledge of System-on-Chip (SoC) architecture and its real-world applications.
- Developed expertise in the LiteX framework for SoC design and FPGA deployment.
- Acquired proficiency in the GCC toolchain and successfully designed a RISC-V single-cycle processor.

Internship Internee

National Incubation Center for Aerospace Technologies (NICAT)

07/2023 - 09/2023

Achievements/Tasks

- Conducted research on chip design and RISC-V architecture, exploring optimization techniques.
- Gained insights into transforming Final Year Projects (FYPs) into viable startups.
- Organized and promoted workshops, ensuring effective audience engagement.

SKILLS

RISC-V

Embedded C

SoC

Python

Processor Design

LiteX

Verilog

FPGA

PERSONAL PROJECTS

RISC-V Single Cycle Processor Design (10/2024 - Present)

Designed RISC-V Based SoC Using LiteX and Deployed it on FPGA. (09/2024 - Present)

Human Vision Vs Machine Creation, Realism Classification Approach. (11/2024 - 02/2025)

Designed 5 Stage RISC-V Pipelined Processor (12/2024 - Present)

Designed a Robotic car using motor driver L293D (06/2023 - 07/2023)

CERTIFICATES

Stanford Certified for Machine Learning: Regression and Classification. (06/2024 - Present)

Certificate of Learning Digital Marketing Strategies (05/2022 - 07/2022)

IEEE Certified Volunteer Administrator (10/2022 - Present)

LANGUAGES

English

Native or Bilingual Proficiency

Urdu

Native or Bilingual Proficiency

INTERESTS

Digital System Design

Computer Architecture

RTL

UVM

Research

Verification and Testing

Partnerships and Collaborations

Project Management